

SPT-P5548-60(D)

2.67Gbps SFP Optical Transceiver, 60km Reach

Features

- Up to 2.67 Gb/s data links
- 1550nm DFB laser and PIN photo detector for 60km transmission
- Compliant with SFP MSA and SFF-8472 with duplex LC receptacle
- Digital Diagnostic Monitoring: Internal Calibration or External Calibration
- Compatible with RoHS
- +3.3V single power supply
- Operating case temperature:
 - Standard: 0 to +70°C
 - Industrial: -40 to +85°C

Applications

- SDH STM-16 and SONET OC-48 system
- 2X Fiber Channel
- Switch to Switch interface
- Switched backplane applications
- Router/Server interface
- Other optical transmission systems

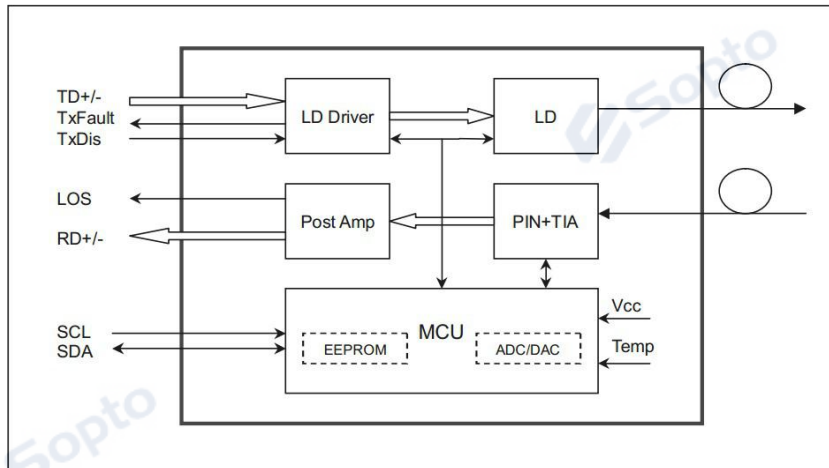
Description

The SFP transceivers are high performance, cost effective modules supporting dual data-rate of 2.67Gbps and 60km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA

Module Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	V _{cc}	-0.5	4.5	V
Storage Temperature	T _s	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	T _c	0		+70	°C
	Industrial		-40		+85	°C
Power Supply Voltage		V _{cc}	3.13	3.3	3.47	V
Power Supply Current		I _{cc}			170	mA
Data Rate				2.67		Gbps

Optical and Electrical Characteristics

Parameter	Symol	Min	Typical	Max	Unit	Notes
Transmitter						
Centre Wavelength	λ_c	1430	1500	1580	nm	
Spectral Width (-20dB)	$\Delta\lambda$			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Average Output Power	P _{out}	-3		0	dBm	1
Extinction Ratio	ER	9			dB	
Optical Rise/Fall Time (20%~80%)	tr/tf			0.16	ns	

Data Input Swing Differential	V_{IN}	400		1800	mV	2
Input Differential Impedance	Z_{IN}	90	100	110	Ω	
TX Disable	Disable		2.0	V_{cc}	V	
	Enable		0	0.8	V	
TX Fault	Fault		2.0	V_{cc}	V	
	Normal		0	0.8	V	
Receiver						
Centre Wavelength	λ_c	1260		1580	nm	
Receiver Sensitivity				-24	dBm	3
Receiver Overload		-1			dBm	3
LOS De-Assert	LOSD			-25	dBm	
LOS Assert	LOSA	-36			dBm	
LOS Hysteresis		1		4	dB	
Data Output Swing Differential	V_{out}	370		1800	mV	4
LOS	High	2.0		V_{cc}	V	
	Low			0.8	V	

Notes:

1. The optical power is launched into SMF.
2. PECL input, internally AC-coupled and terminated.
3. Measured with a PRBS 223-1 test pattern @2488Mbps, BER $\leq 1 \times 10^{-12}$
4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
TX Disable Negate Time	t_{on}			1	ms
TX Disable Assert Time	t_{off}			10	μs
Time To Initialize, including Reset of TX Fault	t_{init}			300	ms
TX Fault Assert Time	t_{fault}			100	μs
TX Disable To Reset	t_{reset}	10			μs
LOS Assert Time	t_{loss_on}			100	μs
LOS De-assert Time	t_{loss_off}			100	μs
Serial ID Clock Rate	f_{serial_clock}			400	KHz
MOD_DEF (0:2)-High	VH	2		V_{cc}	V
MOD_DEF (0:2)-Low	VL			0.8	V

Diagnostics

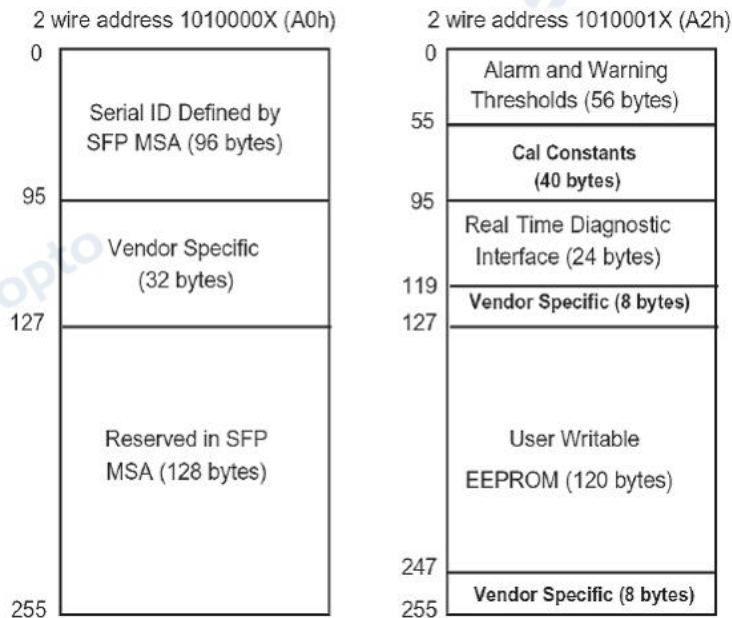
Parameter	Range	Unit	Accuracy	Calibration
Temperature	0 to +70	°C	±3°C	Internal / External
	-40 to +85			
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 100	mA	±10%	Internal / External
TX Power	-3 to 0	dBm	±3dB	Internal / External
RX Power	-24 to -1	dBm	±3dB	Internal / External

Digital Diagnostic Memory Map

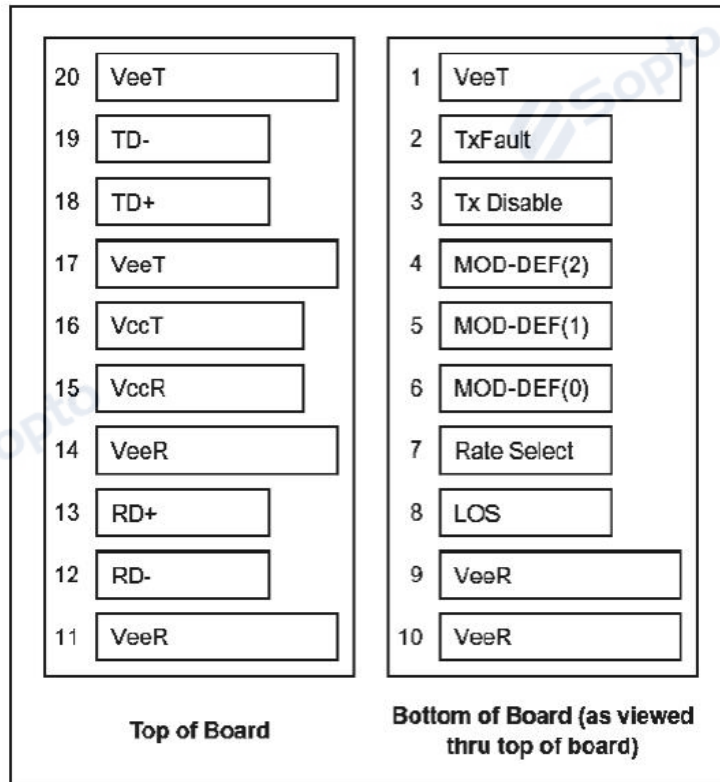
The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.



Pin Definitions



Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	VEER	Receiver ground	1	
10	VEER	Receiver ground	1	
11	VEER	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	VEER	Receiver ground	1	
15	VCCR	Receiver Power Supply	2	
16	VCCT	Transmitter Power Supply	2	
17	VEET	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6

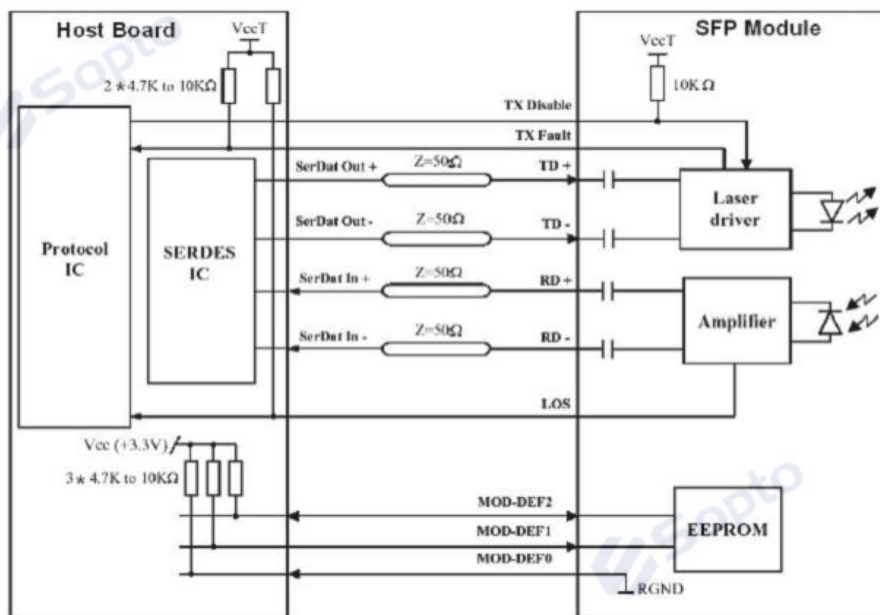
19	TD-	Inv. Transmit Data In	3	Note 6
20	VEET	Transmitter Ground	1	

Notes:

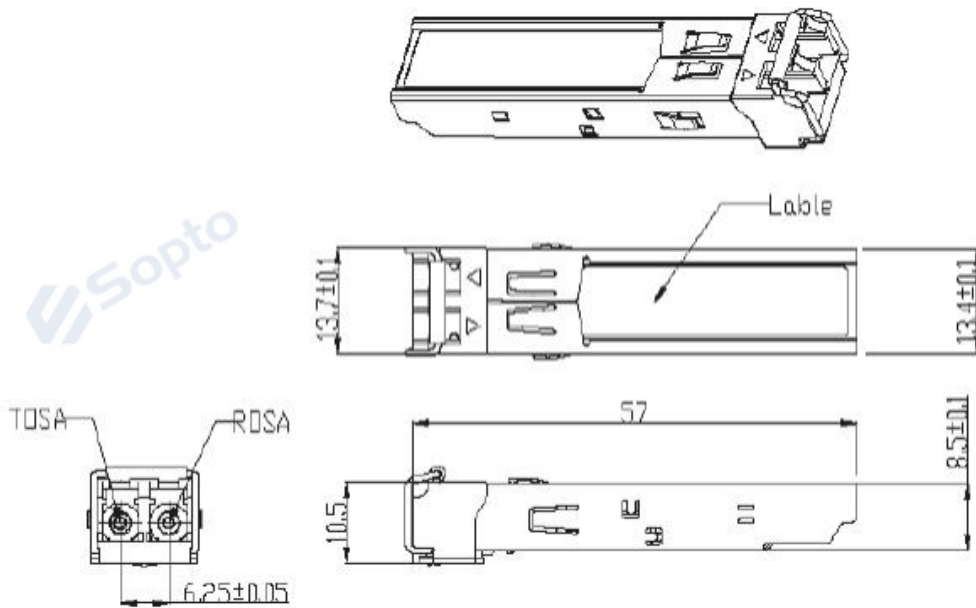
Plug Seq.: Pin engagement sequence during hot plugging.

- 1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.
- 2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7k~10kΩ resistor. Its states are:
 Low (0 to 0.8V): Transmitter on
 (>0.8V, < 2.0V): Undefined
 High (2.0 to 3.465V): Transmitter Disabled
 Open: Transmitter Disabled
- 3) Mod-Def. 0, 1, 2. These are the module definition pins. They should be pulled up with a 4.7k~10kΩ resistor on the host board.
 The pull-up voltage shall be VccT or VccR.
 Mod-Def. 0 is grounded by the module to indicate that the module is present
 Mod-Def. 1 is the clock line of two wire serial interface for serial ID
 Mod-Def. 2 is the data line of two wire serial interface for serial ID
- 4) LOS is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+ : These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6) TD-/+ : These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

Recommended Interface Circuit



Mechanical Dimensions



Ordering information

Part number	Product Description
SPT-P5548-60	1550nm, 2.5Gbps, 60km, 0°C ~ +70°C
SPT-P5548-60D	1550nm, 2.5Gbps, 60km, 0°C ~ +70°C, DDM
SPT-P5548-60T	1550nm, 2.5Gbps, 60km, -40°C ~ +85°C
SPT-P5548-60TD	1550nm, 2.5Gbps, 60km, -40°C ~ +85°C, DDM

Note: If you need more customized services, please contact us.

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