

SPT-PBXX-X60D

155Mbps SFP Bi-Directional Transceiver,60km Reach

1310nm TX / 1550 nm RX & 1550nm TX / 1310 nm RX

Features

- Up to 155Mbps data-rate
- 1310nm or 1550 DFB laser and PIN photo detector for 60km transmission
- Compliant with SFP MSA and SFF-8472 with simplex LC (SC) receptacle
- Digital Diagnostic Monitoring: Internal Calibration or External Calibration
- Compatible with RoHS
- +3.3V single power supply
- Operating case temperature:

Standard: $0 \text{ to } +70^{\circ}\text{C}$

Applications

- SDH STM-1, S-1.1, L-1.1, L-1.2
- SONET OC-3 IR1, LR1, LR2
- Other optical links

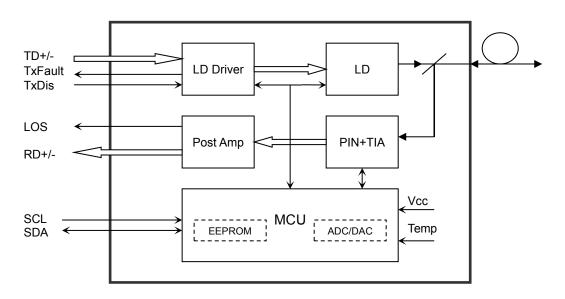
Description

The SFP-BIDI transceivers are high performance, cost effective modules supporting data-rate of 155Mbps and 60km transmission distance with SMF.

The transceiver consists of three sections: a DFB laser transmitter, a PIN photodiode integrated with a trans-impedance preamplifier (TIA) and MCU control unit. All modules satisfy class I laser safety requirements.

The transceivers are compatible with SFP Multi-Source Agreement (MSA) and SFF-8472. For further information, please refer to SFP MSA.





Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage	Vcc	-0.5	4.5	V
Storage Temperature	Ts	-40	+85	°C
Operating Humidity	-	5	85	%

Recommended Operating Conditions

Parameter		Symbol	Min	Typical	Max	Unit
Operating Case Temperature	Standard	Тс	0		+70	°C
Power Supply Voltage		Vcc	3.13	3.3	3.47	V
Power Supply Current		Icc			160	mA
Data Rate				155		Mbps

Optical and Electrical Characteristics

Parameter	Symbol	Min	Typical	Max	Unit	Notes		
	Transmitter							
Contro Wayalanath	λς	1260	1310	1360	nm	1310nm TX		
Centre Wavelength	λα	1530	1550	1570	nm	1550nm TX		
Spectral Width (RMS)	Δλ			4	nm			
Average Output Power	Pout	-3		0	dBm	1		
Extinction Ratio	ER	10			dВ			



	Input Swing fferential	$V_{\rm IN}$	400		1800	mV	2
-	Differential npedance	ZIN	90	100	110	Ω	
TX	Disable		2.0		Vcc	V	
Disable	Enable		0		0.8	V	
TV E14	Fault		2.0		Vcc	V	
TX Fault	Normal		0		0.8	V	
	Receiver						
Receive	er Sensitivity				-34	dBm	3
Receiv	er Overload		-3			dBm	3
LOS	De-Assert	LOSD			-35	dBm	
LO	S Assert	LOSA	-46			dBm	
LOS	Hysteresis		1		4	dB	
	output Swing Differential	Vout	400		1800	mV	4
	1.00		2.0		Vcc	V	
	LOS	Low			0.8	V	

Notes:

- 1. The optical power is launched into SMF.
- 2. PECL input, internally AC-coupled and terminated.
- 3. Measured with a PRBS 2^{23} -1 test pattern @155Mbps, BER $\leq 1 \times 10^{-10}$.
- 4. Internally AC-coupled.

Timing and Electrical

Parameter	Symbol	Min	Typical	Max	Unit
Tx Disable Negate Time	t_on			1	ms
Tx Disable Assert Time	t_off			10	μs
Time To Initialize, including Reset of Tx Fault	t_init			300	ms
Tx Fault Assert Time	t_fault			100	μs
Tx Disable To Reset	t_reset	10			μs
LOS Assert Time	t_loss_on			100	μs
LOS De-assert Time	t_loss_off			100	μs
Serial ID Clock Rate	f_serial_clock			400	KHz
MOD_DEF (0:2)-High	VH	2		Vcc	V
MOD_DEF (0:2)-Low	VL			0.8	V

Diagnostics

Parameter	Range	Unit	Accuracy	Calibration
	Add.: 2nd Floor Building D Huaf Tel: (0086) 755 2301834		Park, Xixiang Baoan District Shenzl Email: info@sopto.com.cn	hen



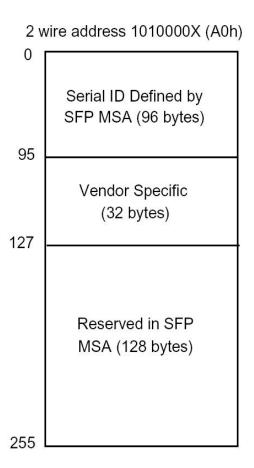
Temperature	0 to +70	°C	±3°C	Internal / External
Voltage	3.0 to 3.6	V	±3%	Internal / External
Bias Current	0 to 100	mA	±10%	Internal / External
TX Power	-3 to 0	dBm	±3dB	Internal / External
RX Power	-34 to -3	dBm	±3dB	Internal / External

Digital Diagnostic Memory Map

The transceivers provide serial ID memory contents and diagnostic information about the present operating conditions by the 2-wire serial interface (SCL, SDA).

The diagnostic information with internal calibration or external calibration all are implemented, including received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring.

The digital diagnostic memory map specific data field defines as following.

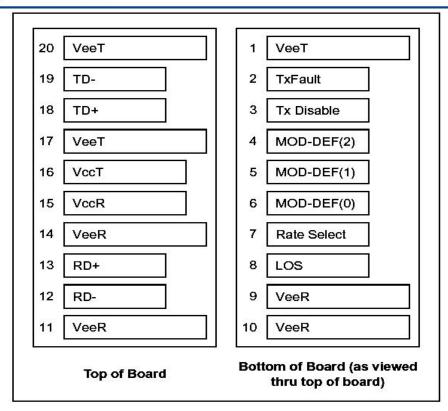


	2 wire address 1010001X (A2h)					
55	Alarm and Warning Thresholds (56 bytes)					
95	Cal Constants (40 bytes)					
	Real Time Diagnostic Interface (24 bytes)					
119 127	Vendor Specific (8 bytes)					
	User Writable EEPROM (120 bytes)					
247						
255	Vendor Specific (8 bytes)					

Pin Definitions

Pin Diagram





Pin Descriptions

Pin	Signal Name	Description	Plug Seq.	Notes
1	VEET	Transmitter Ground	1	
2	TX FAULT	Transmitter Fault Indication	3	Note 1
3	TX DISABLE	Transmitter Disable	3	Note 2
4	MOD_DEF(2)	SDA Serial Data Signal	3	Note 3
5	MOD_DEF(1)	SCL Serial Clock Signal	3	Note 3
6	MOD_DEF(0)	TTL Low	3	Note 3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	Note 4
9	VEER	Receiver ground	1	
10	VEER	Receiver ground	1	
11	VEER	Receiver ground	1	
12	RD-	Inv. Received Data Out	3	Note 5
13	RD+	Received Data Out	3	Note 5
14	VEER	Receiver ground	1	
15	VCCR	Receiver Power Supply	2	
16	VCCT	Transmitter Power Supply	2	
17	VEET	Transmitter Ground	1	
18	TD+	Transmit Data In	3	Note 6
19	TD-	Inv. Transmit Data In	3	Note 6
20	VEET	Transmitter Ground	1	

Notes:

Plug Seq.: Pin engagement sequence during hot plugging.

1) TX Fault is an open collector output, which should be pulled up with a 4.7k~10kΩ resistor on the host board to a



voltage between 2.0V and Vcc+0.3V. Logic 0 indicates normal operation; Logic 1 indicates a laser fault of some kind. In the low state, the output will be pulled to less than 0.8V.

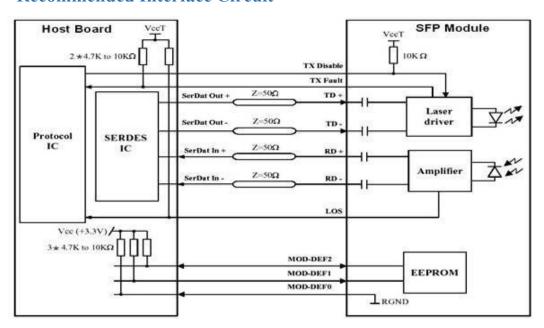
2) TX Disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a $4.7k\sim10k\Omega$ resistor. Its states are:

Low (0 to 0.8V): Transmitter on (>0.8V, <2.0V): Undefined

High (2.0 to 3.465V): Transmitter Disabled Open: Transmitter Disabled

- 3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a $4.7k\sim10k\Omega$ resistor on the host board. The pull-up voltage shall be VccT or VccR.
 - Mod-Def 0 is grounded by the module to indicate that the module is present
 - Mod-Def 1 is the clock line of two wire serial interface for serial ID
 - Mod-Def 2 is the data line of two wire serial interface for serial ID
- 4) LOS is an open collector output, which should be pulled up with a $4.7k\sim10k\Omega$ resistor. Pull up voltage between 2.0V and Vcc+0.3V. Logic 1 indicates loss of signal; Logic 0 indicates normal operation. In the low state, the output will be pulled to less than 0.8V.
- 5) RD-/+: These are the differential receiver outputs. They are internally AC-coupled 100 differential lines which should be terminated with 100Ω (differential) at the user SERDES.
- 6) TD-/+: These are the differential transmitter inputs. They are internally AC-coupled, differential lines with 100Ω differential termination inside the module.

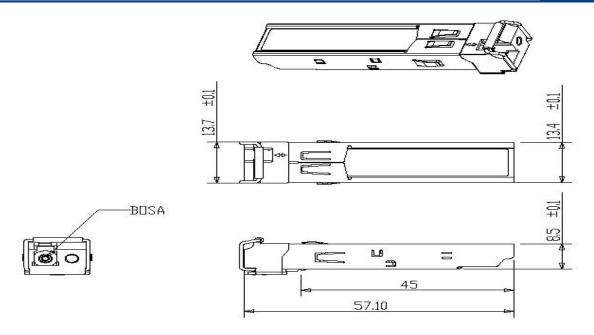
Recommended Interface Circuit



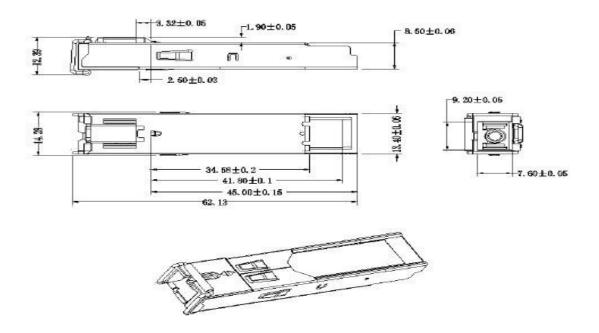
Mechanical Dimensions

A.LC Connector





B.SC Connector



Ordering information

Part Number	Product Description		
1310nm TX / 1550 nm RX			
SPT-PB3503-L60D	1310nm Tx, 155Mbps, LC, 60km, 0°C~+70°C, With DDM		
SPT-PB3503-S60D	1310nm Tx, 155Mbps, SC, 60km, 0°C~+70°C, With DDM		
	1550nm TX / 1310 nm RX		
SPT-PB5303-L60D	1550nm Tx, 155Mbps , LC, 60km, 0°C ~ +70°C, With DDM		
SPT-PB5303-S60D	1550nm Tx, 155Mbps , SC, 60km, 0°C ~ +70°C, With DDM		



Note:

- 1. Default operating case temperature is $0 \sim 70\,^{\circ}$ C. If you need -40 ~85 $^{\circ}$ C products , please contact us.
- 2. If you need more customized services, please contact us.

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